

Serial No.: TBA
 Infineon Docket No.: 2002P50545US
 OC Docket No.: 1115747-0005

Page 1 of 1

FORM PTO-1449 (REV. 2-82)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty. Docket No. 2002 P 50545 US	Serial No. To be assigned
		Applicants: Kiehl, et al.	
		Filing Date: June 26, 2003	Group: To be assigned

INFORMATION DISCLOSURE STATEMENT
 BY APPLICANT

UNITED STATES PATENT DOCUMENTS

*Examiner Initial	Document No.							Date	Name	Class	Sub-Class	Filing Date, if Appropriate
<i>AS</i>	5	7	3	6	8	7	1	04/07/1998	Goto	326	115	
<i>AS</i>	5	3	8	1	1	1	3	01/10/1995	Kimura	330	253	

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Document No.							Date	Country	Class	Sub-Class	Translation Yes No	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

<i>AS</i>	Xilinx, Virtex™-E 1.8 V Field Programmable Gate Arrays; Virtex-E Electrical
<i>AS</i>	Characteristics; DS022-3 (v2.7); pp. 1-25; February 1, 2002.

Examiner:

Date Considered:

7/6/04

*Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.